



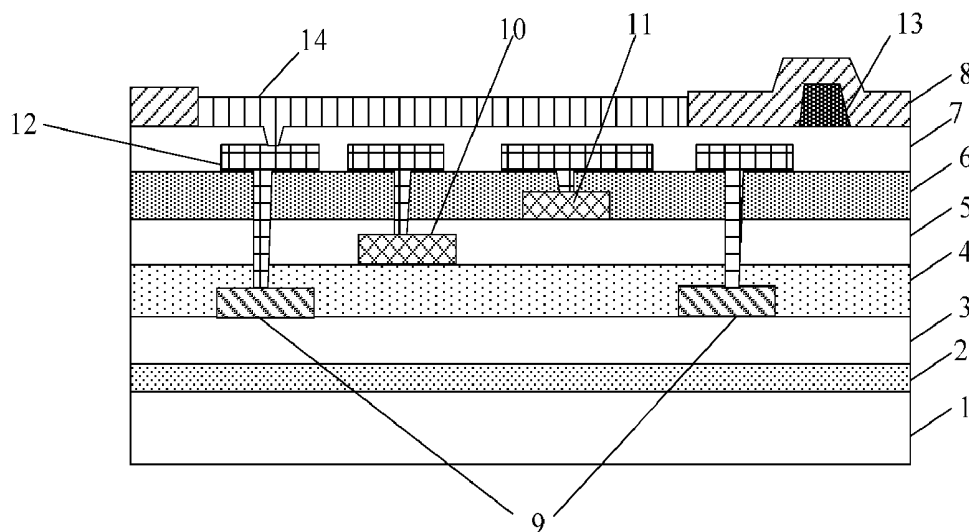
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Liu et al.(10) **Pub. No.: US 2019/0035864 A1**(43) **Pub. Date: Jan. 31, 2019**(54) **ORGANIC LIGHT-EMITTING DIODE
DISPLAY SUBSTRATE, MANUFACTURING
METHOD THEREOF AND DISPLAY DEVICE**(71) Applicant: **BOE TECHNOLOGY GROUP CO.,
LTD.**, Beijing (CN)(72) Inventors: **Wenqu Liu**, Beijing (CN); **Feng
Zhang**, Beijing (CN); **Zhijun Lv**,
Beijing (CN); **Ning Dang**, Beijing
(CN); **Liwen Dong**, Beijing (CN);
Shizheng Zhang, Beijing (CN)(21) Appl. No.: **15/970,863**(22) Filed: **May 3, 2018**(30) **Foreign Application Priority Data**

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(2013.01); **G03F 7/16** (2013.01); **H01L**
2227/323 (2013.01); **H01L 51/56** (2013.01)(57) **ABSTRACT**

The present disclosure provides in some embodiments a method for manufacturing an OLED display substrate, including a step of forming a PS on a substrate with an anode and a step of forming a pattern of a pixel definition layer. The step of forming the PS is performed no later than the step of forming the pattern of the pixel definition layer. According to the present disclosure, the PS is able to be formed prior to the formation of the pattern of the pixel definition layer, or the PS and the pattern of the pixel definition layer are formed simultaneously through one single patterning process.



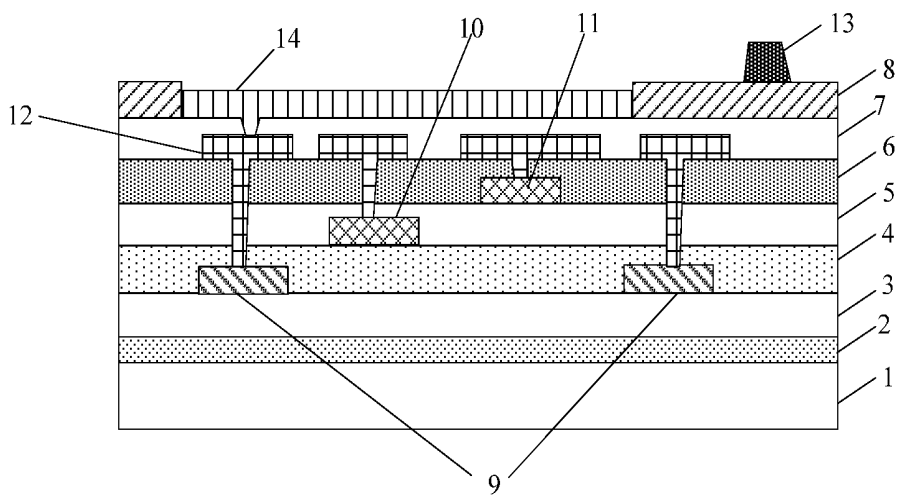


FIG. 1

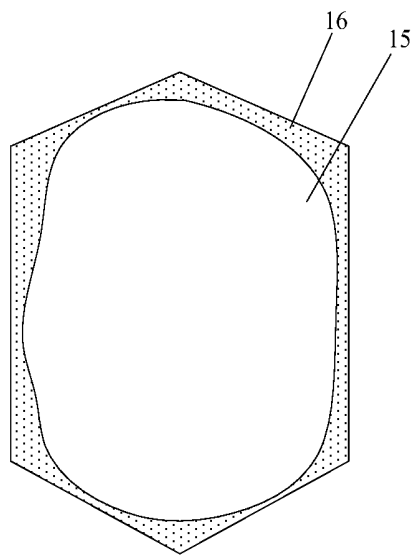


FIG. 2

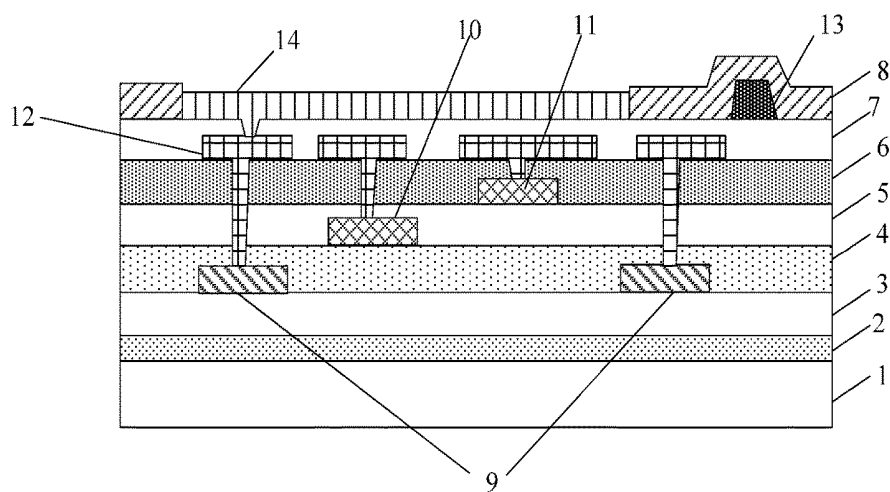


FIG. 3

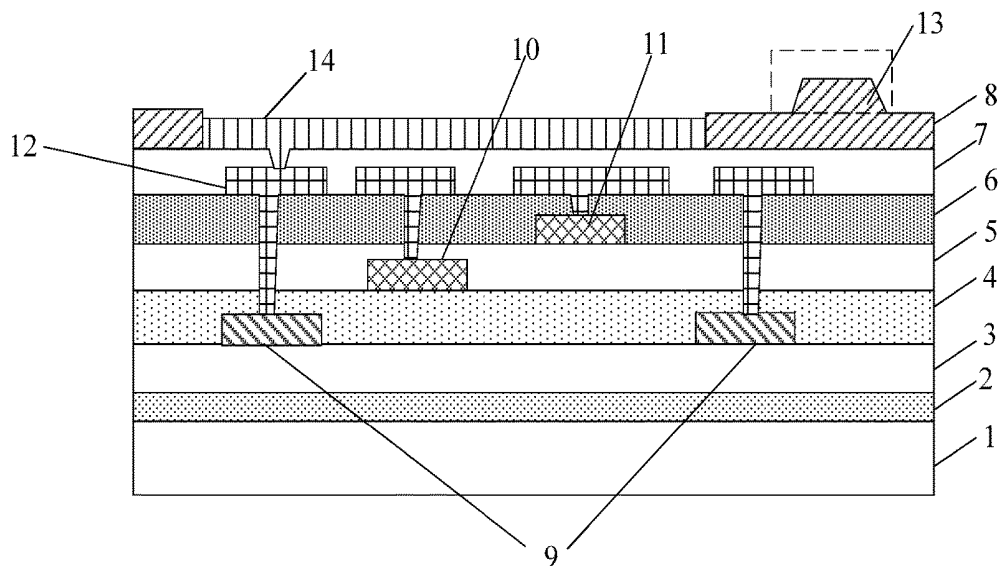


FIG. 4

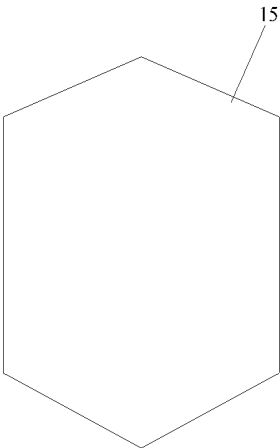


FIG. 5

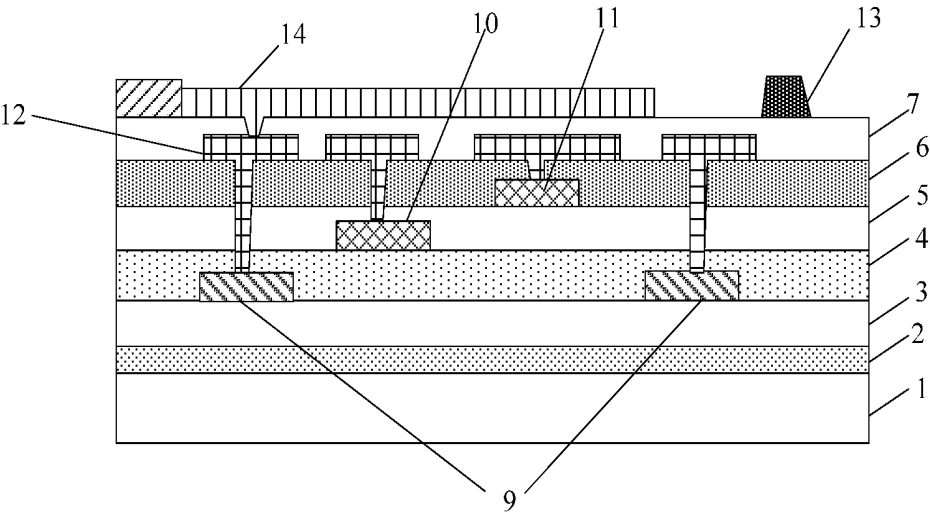


FIG. 6

**ORGANIC LIGHT-EMITTING DIODE
DISPLAY SUBSTRATE, MANUFACTURING
METHOD THEREOF AND DISPLAY DEVICE**

**CROSS-REFERENCE TO RELATED
APPLICATION**

[0001] The present application claims a priority of the Chinese patent application No. 201710631205.3 filed on Jul. 28, 2017, which is incorporated herein by reference in its entirety.

TECHNICAL FIELD

[0002] The present disclosure relates to the field of display technology, in particular to an organic light-emitting diode (OLED) display substrate, a manufacturing method thereof and a display device.

BACKGROUND

[0003] Along with the development of the science and technology, visual information and flat-panel display devices capable of receiving and displaying the visual information have played a more and more important role in our lives. As a new-generation display device, an OLED display device has attracted more and more attentions due to such advantages as low power consumption and high contrast.

[0004] During the manufacture of an OLED display substrate, an anode of the OLED display substrate is formed at first, and then a pattern of a pixel definition layer is formed on a base substrate with the anode. The pattern of the pixel definition layer is provided with a plurality of openings so as to expose the anode. After the formation of the pattern of the pixel definition layer, it is necessary to form a post spacer (PS) on the pattern of the pixel definition layer. During the formation of the PS, it is necessary to coat an organic photosensitive material onto the base substrate. After an exposing and developing process, the organic photosensitive material may easily remain on the anode, so the performance and the yield of the OLED display substrate may be adversely affected.

SUMMARY

[0005] In one aspect, the present disclosure provides in some embodiments a method for manufacturing an organic light-emitting diode display substrate, including a step of forming a post spacer on a substrate with an anode and a step of forming a pattern of a pixel definition layer. The step of forming the post spacer is before the step of forming the pattern of the pixel definition layer, or the step of forming the post spacer and the step of forming the pattern of the pixel definition layer are performed simultaneously.

[0006] In a possible embodiment of the present disclosure, the method includes: coating an organic photosensitive material layer onto the substrate with the anode; exposing and developing the organic photosensitive material layer so as to form the post spacer; and coating an insulation material layer onto the substrate with the post spacer, and patterning the insulation material layer so as to form the pattern of the pixel definition layer.

[0007] In a possible embodiment of the present disclosure, the method includes: coating an insulation layer onto the substrate with the anode; coating a photoresist onto the insulation layer, and exposing and developing the photore-

sist so as to form a photoresist fully-reserved region, a photoresist partially-reserved region and a photoresist unreserved region; etching off the insulation layer at the photoresist unreserved region; removing the photoresist at the photoresist partially-reserved region, and etching off a portion of the insulation layer at the photoresist partially-reserved region; and removing the photoresist at the photoresist fully-reserved region to form a pattern of the insulation layer. The pattern of the insulation layer includes a first portion and a second portion protruding from the first portion, the first portion is formed into the pattern of the pixel definition layer, and the second portion is formed into the post spacer.

[0008] In a possible embodiment of the present disclosure, subsequent to the step of forming the pattern of the pixel definition layer, the method further includes forming an organic light-emitting layer and a cathode sequentially on the substrate with the pattern of the pixel definition layer.

[0009] In a possible embodiment of the present disclosure, a cross section of the post spacer is of a quadrilateral shape.

[0010] In a possible embodiment of the present disclosure, a cross section of the post spacer is of a trapezoidal shape.

[0011] In a possible embodiment of the present disclosure, a cross section of the post spacer is of an isosceles trapezoidal shape.

[0012] In a possible embodiment of the present disclosure, the pattern of the pixel definition layer fully covers the post spacer.

[0013] In a possible embodiment of the present disclosure, the pattern of the pixel definition layer and the post spacer are formed into one piece.

[0014] In a possible embodiment of the present disclosure, a cross section of the first portion is of a rectangular shape.

[0015] In a possible embodiment of the present disclosure, a cross section of the second portion is of a trapezoidal shape.

[0016] In a possible embodiment of the present disclosure, a cross section of the second portion is of an isosceles trapezoidal shape.

[0017] In a possible embodiment of the present disclosure, no organic photosensitive material layer remains at a pixel region corresponding to the anode after the formation of the pattern of the pixel definition layer through patterning the insulation material layer.

[0018] In another aspect, the present disclosure provides in some embodiments an OLED display substrate manufactured using the above-mentioned method.

[0019] In a possible embodiment of the present disclosure, the OLED display substrate includes: a base substrate; a thin film transistor (TFT) array layer arranged on the base substrate; an anode arranged on the base substrate with the TFT array layer; a post spacer arranged on the base substrate with the anode; a pattern of a pixel definition layer arranged on the base substrate with the post spacer and covering the post spacer; and an organic light-emitting layer and a cathode arranged on the base substrate with the pattern of the pixel definition layer.

[0020] In a possible embodiment of the present disclosure, the OLED display substrate includes: a base substrate; a TFT array layer arranged on the base substrate; an anode arranged on the base substrate with the TFT array layer; a pattern of an insulation layer arranged on the base substrate with the anode, the pattern of the insulation layer including a first portion and a second portion protruded from the first

portion, the first portion being the pattern of the pixel definition layer and the second portion being the post spacer; and an organic light-emitting layer and a cathode arranged on the base substrate with the pattern of the insulation layer. [0021] In yet another aspect, the present disclosure provides in some embodiments a display device including the above-mentioned OLED display substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

[0022] FIG. 1 is a schematic view showing a conventional OLED display substrate;

[0023] FIG. 2 is a schematic view showing a pixel region of the conventional OLED display substrate where an organic photosensitive material remains;

[0024] FIG. 3 is a schematic view showing an OLED display substrate according to a first embodiment of the present disclosure;

[0025] FIG. 4 is another schematic view showing the OLED display substrate according to a second embodiment of the present disclosure;

[0026] FIG. 5 is a schematic view showing a pixel region of the OLED display substrate where no organic photosensitive material remains according to one embodiment of the present disclosure; and

[0027] FIG. 6 is a schematic view showing the OLED display substrate acquired after Step 13 according to the first embodiment of the present disclosure.

REFERENCE SIGN LIST

- [0028] 1 rigid substrate
- [0029] 2 flexible substrate
- [0030] 3 buffer layer
- [0031] 4 first gate insulation layer
- [0032] 5 second gate insulation layer
- [0033] 6 intermediate insulation layer
- [0034] 7 planarization layer
- [0035] 8 pixel definition layer
- [0036] 9 active layer
- [0037] 10 first gate electrode
- [0038] 11 second gate electrode
- [0039] 12 source/drain metal layer
- [0040] 13 post spacer (PS)
- [0041] 14 anode
- [0042] 15 pixel region
- [0043] 16 organic photosensitive material

DETAILED DESCRIPTION

[0044] In order to make the objects, the technical solutions and the advantages of the present disclosure more apparent, the present disclosure will be described hereinafter in a clear and complete manner in conjunction with the drawings and embodiments.

[0045] During the manufacture of an OLED display substrate, as shown in FIG. 1, an anode 14 of the OLED display substrate is formed at first, and then a pattern of a pixel definition layer 8 is formed on a base substrate with the anode 14. The pattern of the pixel definition layer 8 is provided with a plurality of openings so as to expose the anode. After the formation of the pattern of the pixel definition layer 8, it is necessary to form a PS 13 on the pattern of the pixel definition layer 8. During the formation of the PS 13, it is necessary to coat an organic photosensitive material 16 onto the base substrate, and at this time, the

organic photosensitive material may also be coated onto the anode 14. The anode 14 is located within the opening of the pixel definition layer 8, i.e., within a structure like a hole. After an exposing and developing process, as shown in FIG. 2, the organic photosensitive material 16 may easily remain on the anode 14 corresponding to a pixel region 15, so the performance and the yield of the OLED display substrate may be adversely affected to some extent.

[0046] In addition, in the case that the remaining organic photosensitive material is to be removed, an additional ashing process needs to be performed. Hence, the manufacture process of the OLED display substrate may become more complex and the manufacture cost thereof may increase.

[0047] An object of the present disclosure is to provide an OLED display substrate, a manufacturing method thereof and a display device, so as to prevent the organic photosensitive material from remaining on the anode, thereby to improve the yield of the OLED display substrate.

[0048] The present disclosure provides in some embodiments a method for manufacturing an OLED display substrate, including a step of forming a PS on a substrate with an anode and a step of forming a pattern of a pixel definition layer. The step of forming the PS is performed no later than the step of forming the pattern of the pixel definition layer.

[0049] According to the method in the embodiments of the present disclosure, the step of forming the PS is performed no later than the step of forming the pattern of the pixel definition layer, so the PS may be formed prior to the formation of the pattern of the pixel definition layer, or the PS and the pattern of the pixel definition layer may be formed simultaneously through one single patterning process. As a result, it is unnecessary to coat an organic photosensitive material onto the substrate after the formation of the pattern of the pixel definition layer, so as to prevent the organic photosensitive material from remaining on the anode, thereby to improve the yield of the OLED display substrate.

[0050] In a possible embodiment of the present disclosure, the step of forming the PS is performed prior to the step of forming the pattern of the pixel definition layer.

[0051] In a possible embodiment of the present disclosure, the method includes: coating an organic photosensitive material layer onto the substrate with the anode; exposing and developing the organic photosensitive material so as to form the PS; and coating an insulation material layer onto the substrate with the PS, and patterning the insulation layer material so as to form the pattern of the pixel definition layer.

[0052] In a possible embodiment of the present disclosure, the PS and the pattern of the pixel definition layer are formed simultaneously through one single patterning process.

[0053] In a possible embodiment of the present disclosure, the method includes: coating an insulation layer onto the substrate with the anode; coating a photoresist onto the insulation layer, and exposed and developing the photoresist so as to form a photoresist fully-reserved region, a photoresist partially-reserved region and a photoresist unreserved region; etching off the insulation layer at the photoresist unreserved region; removing the photoresist at the photoresist partially-reserved region and etching off a portion of the insulation layer at the photoresist partially-reserved region; and removing the photoresist at the photoresist fully-reserved region, so as to form a pattern of the insulation layer, the pattern of the insulation layer including a first portion

and a second portion protruding from the first portion, the first portion being the pattern of the pixel definition layer, and the second portion being the PS.

[0054] In a possible embodiment of the present disclosure, subsequent to the step of forming the pattern of the pixel definition layer, the method further includes forming an organic light-emitting layer and a cathode sequentially on the substrate with the pattern of the pixel definition layer.

[0055] The present disclosure further provides in some embodiments an OLED display substrate manufactured using the above-mentioned method.

[0056] In a possible embodiment of the present disclosure, the OLED display substrate includes: a base substrate; a thin film transistor (TFT) array layer arranged on the base substrate; an anode arranged on the base substrate with the TFT array layer; a PS arranged on the base substrate with the anode; a pattern of a pixel definition layer arranged on the base substrate with the PS and covering the PS; and an organic light-emitting layer and a cathode arranged on the base substrate with the pattern of the pixel definition layer.

[0057] In another possible embodiment of the present disclosure, the OLED display substrate includes: a base substrate; a TFT array layer arranged on the base substrate; an anode arranged on the base substrate with the TFT array layer; a pattern of an insulation layer arranged on the base substrate with the anode, the pattern of the insulation layer including a first portion and a second portion protruded from the first portion, the first portion being the pattern of the pixel definition layer and the second portion being the PS; and an organic light-emitting layer and a cathode arranged on the base substrate with the pattern of the insulation layer.

[0058] The OLED display substrate and the manufacturing method thereof will be described hereinafter in more details in conjunction with the drawings and embodiments.

First Embodiment

[0059] In this embodiment, the PS is formed prior to the pattern of the pixel definition layer. The method may include the following steps.

[0060] Step 1: providing a rigid substrate **1**. To be specific, the rigid substrate **1** may be a glass substrate or quartz substrate.

[0061] Step 2: forming a flexible substrate **2** on the rigid substrate **1**. To be specific, the flexible substrate **2** may be made of polyimide.

[0062] Step 3: forming a buffer layer **3** on the flexible substrate **2**. To be specific, the buffer layer **3** may be made of an oxide, a nitride or an oxynitride.

[0063] Step 4: forming a pattern of an active layer **9** on the buffer layer **3**. To be specific, a semiconductor material layer may be deposited onto the buffer layer **3**, and a photoresist may be coated onto the semiconductor material layer and then exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the pattern of the active layer **9** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region. Next, the semiconductor material layer at the photoresist unreserved region may be etched off through an etching process, so as to form the pattern of the active layer **9**. Finally, the photoresist at the photoresist reserved region may be removed.

[0064] Step 5: forming a first gate insulation layer **4**. To be specific, the first gate insulation layer **4** may be made of an

oxide, a nitride or an oxynitride, with corresponding reaction gases of SiH_4 , NH_3 and N_2 , or SiH_2Cl_2 , NH_3 and N_2 .

[0065] Step 6: forming a pattern of a first gate electrode **10** on the first gate insulation layer **4**. To be specific, a gate metal layer having a thickness of 500 to 4000 Å may be deposited onto the first gate insulation layer **4** through sputtering or thermal evaporation. The gate metal layer may be made of copper (Cu), aluminium (Al), argentum (Ag), molybdenum (Mo), chromium (Cr), neodymium (Nd), nickel (Ni), manganese (Mn), titanium (Ti), tantalum (Ta), tungsten (W) or an alloy thereof, and it may be of a single-layered structure or a multi-layered structure such as Cu/Mo , $\text{Ti}/\text{Cu}/\text{Ti}$ or $\text{Mo}/\text{Al}/\text{Mo}$. Next, a photoresist may be coated onto the gate metal layer, and exposed with a mask plate, so as to form a photoresist reserved region corresponding to a region where the pattern of the first gate electrode **10** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the gate metal film at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the first gate electrode **10**.

[0066] Step 7: forming a second gate insulation layer **5**. To be specific, the second gate insulation layer **5** may be made of an oxide, a nitride or an oxynitride, with corresponding reaction gases of SiH_4 , NH_3 and N_2 , or SiH_2Cl_2 , NH_3 and N_2 .

[0067] Step 8: forming a pattern of a second gate electrode **11** on the second gate insulation layer **5**. To be specific, a gate metal layer having a thickness of 500 to 4000 Å may be deposited onto the second gate insulation layer **5** through sputtering or thermal evaporation. The gate metal layer may be made of Cu, Al, Ag, Mo, Cr, Nd, Ni, Mn, Ti, Ta, W or an alloy thereof, and it may be of a single-layered structure or a multi-layered structure such as Cu/Mo , $\text{Ti}/\text{Cu}/\text{Ti}$ or $\text{Mo}/\text{Al}/\text{Mo}$. Next, a photoresist may be coated onto the gate metal layer, and exposed with a mask plate, so as to form a photoresist reserved region corresponding to a region where the pattern of the second gate electrode **11** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the gate metal layer at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the second gate electrode **11**. In this embodiment, a driving TFT is provided with two gates (i.e., the first gate electrode **10** and the second gate electrode **11**). Of course, the driving TFT may also be provided with merely one gate electrode.

[0068] Step 9: forming an intermediate insulation layer **6**. To be specific, the intermediate insulation layer **6** may be made of an oxide, a nitride or an oxynitride, with corresponding reaction gases of SiH_4 , NH_3 and N_2 , or SiH_2Cl_2 , NH_3 and N_2 .

[0069] Step 10: forming a pattern of a source/drain metal layer **12** on the intermediate insulation layer **6**. To be specific, the source/drain metal layer **12** having a thickness of about 2000 to 4000 Å may be deposited onto the intermediate insulation layer **6** through magnetron-sputter-

ing, thermal evaporation or any other film-forming method. The source/drain metal layer **12** may be made of Cu, Al, Ag, Mo, Cr, Nd, Ni, Mn, Ti, Ta, W or an alloy thereof, and it may be of a single-layered structure or a multi-layered structure such as Cu\Mo, Ti\Cu\Ti or Mo\Al\Mo. Next, a photoresist may be coated onto the source/drain metal layer **12**, and exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the pattern of the source/drain metal layer **12** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the source/drain metal layer **12** at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the source/drain metal layer **12**. The pattern of the source/drain metal layer **12** includes a drain electrode, a source electrode and a data line. The source electrode and the drain electrode are connected to the active layer **9** through via-holes penetrating through the intermediate insulation layer **6**, the second gate insulation layer **5** and the first gate insulation layer **4**.

[0070] Step 11: forming a pattern of a planarization layer **7**. To be specific, the planarization layer **7** may be made of organic resin.

[0071] Step 12: forming a pattern of an anode **14**. To be specific, a transparent conductive layer having a thickness of about 300 to 1500 Å may be deposited onto the planarization layer **7** through sputtering or thermal evaporation. The transparent conductive layer may be made of indium tin oxide (ITO), indium zinc oxide (IZO) or any other transparent metal oxide. Next, a photoresist may be coated onto the transparent conductive layer, and exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the anode **14** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the transparent conductive layer at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the anode **14**.

[0072] Step 13: forming a pattern of a PS **13**. To be specific, an organic photosensitive material layer may be coated onto the flexible substrate acquired after Step 12, and then exposed and developed so as to form an organic photosensitive material reserved region and an organic photosensitive material unreserved region. The organic photoresist material layer at the organic photosensitive material reserved region may form the PS **13**. As shown in FIG. 6, the pattern of the PS **13** has been formed but the pattern of the pixel definition layer **8** has not been formed yet. In other words, the PS **13** is formed prior to the formation of the pattern of the pixel definition layer **8**. In this regard, it is unnecessary to coat the organic photosensitive material onto the substrate after the formation of the pattern of the pixel definition layer **8**.

[0073] Step 14: forming a pattern of a pixel definition layer **8**. To be specific, an insulation material layer may be coated onto the flexible substrate acquired after Step 13. Next, a photoresist may be coated onto the insulation

material layer, and exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the pattern of the pixel definition layer **8** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the insulation material layer at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the pixel definition layer **8**.

[0074] The OLED display substrate in FIG. 3 may be acquired through the above Steps 1 to 14. Subsequently, an organic light-emitting layer and a cathode may be formed on the OLED display substrate. According to the method in the first embodiment of the present disclosure, the PS is formed prior to the formation of the pattern of the pixel definition layer, so it is unnecessary to coat the organic photosensitive material onto the substrate after the formation of the pattern of the pixel definition layer. As shown in FIG. 5, it is able to prevent the organic photosensitive material from remaining on the anode at a pixel region **15**, thereby to improve the yield of the OLED display substrate.

Second Embodiment

[0075] In this embodiment, the PS and the pattern of the pixel definition layer are formed simultaneously through one single patterning process. The method may include the following steps.

[0076] Step 1: providing the rigid substrate **1**. To be specific, the rigid substrate **1** may be a glass substrate or quartz substrate.

[0077] Step 2: forming the flexible substrate **2** on the rigid substrate **1**. To be specific, the flexible substrate **2** may be made of polyimide.

[0078] Step 3: forming the buffer layer **3** on the flexible substrate **2**. To be specific, the buffer layer **3** may be made of an oxide, a nitride or an oxynitride.

[0079] Step 4: forming the pattern of the active layer **9** on the buffer layer **3**. To be specific, a semiconductor material layer may be deposited onto the buffer layer **3**, and a photoresist may be coated onto the semiconductor material layer and then exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the pattern of the active layer **9** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region. Next, the semiconductor material layer at the photoresist unreserved region may be etched off through an etching process, so as to form the pattern of the active layer **9**. Finally, the photoresist at the photoresist reserved region may be removed.

[0080] Step 5: forming the first gate insulation layer **4**. To be specific, the first gate insulation layer **4** may be made of an oxide, a nitride or an oxynitride, with corresponding reaction gases of SiH₄, NH₃ and N₂, or SiH₂Cl₂, NH₃ and N₂.

[0081] Step 6: forming the pattern of the first gate electrode **10** on the first gate insulation layer **4**. To be specific, a gate metal layer having a thickness of 500 to 4000 Å may be deposited onto the first gate insulation layer **4** through sputtering or thermal evaporation. The gate metal layer may

be made of Cu, Al, Ag, Mo, Cr, Nd, Ni, Mn, Ti, Ta, W or an alloy thereof, and it may be of a single-layered structure or a multi-layered structure such as Cu/Mo, Ti/Cu/Ti or Mo/Al/Mo. Next, a photoresist may be coated onto the gate metal layer, and exposed with a mask plate, so as to form a photoresist reserved region corresponding to a region where the pattern of the first gate electrode **10** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the gate metal layer at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the first gate electrode **10**.

[0082] Step 7: forming the second gate insulation layer **5**. To be specific, the second gate insulation layer **5** may be made of an oxide, a nitride or an oxynitride, with corresponding reaction gases of SiH₄, NH₃ and N₂, or SiH₂Cl₂, NH₃ and N₂.

[0083] Step 8: forming the pattern of the second gate electrode **11** on the second gate insulation layer **5**. To be specific, a gate metal layer having a thickness of 500 to 4000 Å may be deposited onto the second gate insulation layer **5** through sputtering or thermal evaporation. The gate metal layer may be made of Cu, Al, Ag, Mo, Cr, Nd, Ni, Mn, Ti, Ta, W or an alloy thereof, and it may be of a single-layered structure or a multi-layered structure such as Cu/Mo, Ti/Cu/Ti or Mo/Al/Mo. Next, a photoresist may be coated onto the gate metal layer, and exposed with a mask plate, so as to form a photoresist reserved region corresponding to a region where the pattern of the second gate electrode **11** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the gate metal layer at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the second gate electrode **11**. In this embodiment, a driving TFT is provided with two gates (i.e., the first gate electrode **10** and the second gate electrode **11**). Of course, the driving TFT may also be provided with merely one gate electrode.

[0084] Step 9: forming the intermediate insulation layer **6**. To be specific, the intermediate insulation layer **6** may be made of an oxide, a nitride or an oxynitride, with corresponding reaction gases of SiH₄, NH₃ and N₂, or SiH₂Cl₂, NH₃ and N₂.

[0085] Step 10: forming the pattern of the source/drain metal layer **12** on the intermediate insulation layer **6**. To be specific, the source/drain metal layer **12** having a thickness of about 2000 to 4000 Å may be deposited onto the intermediate insulation layer **6** through magnetron-sputtering, thermal evaporation or any other film-forming method. The source/drain metal layer **12** may be made of Cu, Al, Ag, Mo, Cr, Nd, Ni, Mn, Ti, Ta, W or an alloy thereof, and it may be of a single-layered structure or a multi-layered structure such as Cu/Mo, Ti/Cu/Ti or Mo/Al/Mo. Next, a photoresist may be coated onto the source/drain metal layer **12**, and exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the pattern of the source/drain metal layer **12** is located and a photoresist

unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the source/drain metal layer **12** at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the source/drain metal layer **12**. The pattern of the source/drain metal layer **12** includes a drain electrode, a source electrode and a data line. The source electrode and the drain electrode are connected to the active layer **9** through via-holes penetrating through the intermediate insulation layer **6**, the second gate insulation layer **5** and the first gate insulation layer **4**.

[0086] Step 11: forming the pattern of the planarization layer **7**. To be specific, the planarization layer **7** may be made of organic resin.

[0087] Step 12: forming the pattern of the anode **14**. To be specific, a transparent conductive layer having a thickness of about 300 to 1500 Å may be deposited onto the planarization layer **7** through sputtering or thermal evaporation. The transparent conductive layer may be made of ITO, IZO or any other transparent metal oxide. Next, a photoresist may be coated onto the transparent conductive layer, and exposed with a mask plate so as to form a photoresist reserved region corresponding to a region where the anode **14** is located and a photoresist unreserved region corresponding to the other region. Next, the photoresist may be developed, so as to fully remove the photoresist at the photoresist unreserved region and maintain a thickness of the photoresist at the photoresist reserved region. Finally, the transparent conductive layer at the photoresist unreserved region may be etched off through an etching process, and the remaining photoresist may be removed, so as to form the pattern of the anode **14**.

[0088] Step 13: forming the PS **13** and the pattern of the pixel definition layer **8**. To be specific, an insulation layer may be coated onto the flexible substrate acquired after Step 12. Next, a photoresist may be coated onto the insulation layer, and exposed and developed so as to form a photoresist fully-reserved region, a photoresist partially-reserved region and a photoresist unreserved region. Next, the insulation layer at the photoresist unreserved region may be etched off, and a portion of the photoresist at the photoresist partially-reserved region may be etched off. Finally, the photoresist at the photoresist fully-reserved region may be removed, so as to form a pattern of the insulation layer. The pattern of the insulation layer may include a first portion and a second portion protruding from the first portion. The first portion may form the pattern of the pixel definition layer **8**, and the second portion may form the PS **13**.

[0089] The OLED display substrate in FIG. 3 may be acquired through the above Steps 1 to 13. Subsequently, an organic light-emitting layer and a cathode may be formed on the OLED display substrate. According to the method in the second embodiment of the present disclosure, the PS and the pattern of the pixel definition layer are formed simultaneously through one signal patterning process. To be specific, a dotted box in FIG. 4 illustratively shows the PS **13**, so it is unnecessary to coat the organic photosensitive material onto the substrate after the formation of the pattern of the pixel definition layer. As shown in FIG. 5, it is able to prevent the organic photosensitive material from remaining

on the anode at the pixel region **15**, thereby to improve the yield of the OLED display substrate.

[0090] The present disclosure further provides in some embodiments a display device including the above-mentioned OLED display substrate. The display device may be a product or member having a display function, e.g., television, display, digital photo frame, mobile phone or flat-panel computer. The display device may further include a flexible circuit board, a printed circuit board and a back plate.

[0091] In the embodiments of the present disclosure, the order of the steps is not limited to the serial numbers thereof. For a person skilled in the art, any change in the order of the steps shall also fall within the scope of the present disclosure if without any creative effort.

[0092] Unless otherwise defined, any technical or scientific term used herein shall have the common meaning understood by a person of ordinary skills. Such words as “first” and “second” used in the specification and claims are merely used to differentiate different components rather than to represent any order, number or importance. Similarly, such words as “one” or “one of” are merely used to represent the existence of at least one member, rather than to limit the number thereof. Such words as “connect” or “connected to” may include electrical connection, direct or indirect, rather than to be limited to physical or mechanical connection. Such words as “on”, “under”, “left” and “right” are merely used to represent relative position relationship, and when an absolute position of the object is changed, the relative position relationship will be changed too.

[0093] It should be appreciated that, in the case that such an element as layer, film, region or substrate is arranged “on” or “under” another element, it may be directly arranged “on” or “under” the other substrate, or an intermediate element may be arranged therebetween.

[0094] The above are merely the preferred embodiments of the present disclosure, but the present disclosure is not limited thereto. Obviously, a person skilled in the art may make further modifications and improvements without departing from the spirit of the present disclosure, and these modifications and improvements shall also fall within the scope of the present disclosure.

What is claimed is:

1. A method for manufacturing an organic light-emitting diode display substrate, comprising:

forming a post spacer on a substrate with an anode; and forming a pattern of a pixel definition layer, wherein the step of forming the post spacer is performed before the step of forming the pattern of the pixel definition layer, or the step of forming the post spacer and the step of forming the pattern of the pixel definition layer are performed simultaneously.

2. The method according to claim **1**, comprising:

coating an organic photosensitive material layer onto the substrate with the anode;

exposing and developing the organic photosensitive material layer so as to form the post spacer; and

coating an insulation material layer onto the substrate with the post spacer, and patterning the insulation material layer so as to form the pattern of the pixel definition layer.

3. The method according to claim **1**, comprising:

coating an insulation layer onto the substrate with the anode;

coating a photoresist onto the insulation layer, and exposing and developing the photoresist so as to form a photoresist fully-reserved region, a photoresist partially-reserved region and a photoresist unreserved region;

etching off the insulation layer at the photoresist unreserved region;

removing the photoresist at the photoresist partially-reserved region, and etching off a portion of the insulation layer at the photoresist partially-reserved region; and

removing the photoresist at the photoresist fully-reserved region to form a pattern of the insulation layer,

wherein the pattern of the insulation layer comprises a first portion and a second portion protruding from the first portion, the first portion is formed into the pattern of the pixel definition layer, and the second portion is formed into the post spacer.

4. The method according to claim **1**, wherein subsequent to forming the pattern of the pixel definition layer, the method further comprises forming an organic light-emitting layer and a cathode sequentially on the substrate with the pattern of the pixel definition layer.

5. The method according to claim **2**, wherein subsequent to forming the pattern of the pixel definition layer, the method further comprises forming an organic light-emitting layer and a cathode sequentially on the substrate with the pattern of the pixel definition layer.

6. The method according to claim **3**, wherein subsequent to forming the pattern of the pixel definition layer, the method further comprises forming an organic light-emitting layer and a cathode sequentially on the substrate with the pattern of the pixel definition layer.

7. The method according to claim **1**, wherein a cross section of the post spacer is of a quadrilateral shape.

8. The method according to claim **7**, wherein a cross section of the post spacer is of a trapezoidal shape.

9. The method according to claim **8**, wherein a cross section of the post spacer is of an isosceles trapezoidal shape.

10. The method according to claim **1**, wherein the pattern of the pixel definition layer fully covers the post spacer.

11. The method according to claim **1**, wherein the pattern of the pixel definition layer and the post spacer are formed into one piece.

12. The method according to claim **3**, wherein a cross section of the first portion is of a rectangular shape.

13. The method according to claim **3**, wherein a cross section of the second portion is of a trapezoidal shape.

14. The method according to claim **3**, wherein a cross section of the second portion is of an isosceles trapezoidal shape.

15. The method according to claim **2**, wherein no organic photosensitive material layer remains at a pixel region corresponding to the anode after the formation of the pattern of the pixel definition layer through patterning the insulation material layer.

16. The method according to claim 1, wherein the post spacer and the pattern of the pixel definition layer are formed simultaneously through a single patterning process, and no organic photosensitive material layer remains at a pixel region corresponding to the anode after the formation of the post spacer and the pattern of the pixel definition layer simultaneously through the single patterning process.

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当前申请(专利权)人(译)	京东方科技集团股份有限公司.		
[标]发明人	LIU WENQU ZHANG FENG LV ZHIJUN DANG NING DONG LIWEN ZHANG SHIZHENG		
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摘要(译)

在一些实施例中，本公开提供了一种用于制造OLED显示器基板的方法，包括在具有阳极的基板上形成PS的步骤以及形成像素限定层的图案的步骤。形成PS的步骤不迟于形成像素限定层的图案的步骤执行。根据本公开，能够在形成像素限定层的图案之前形成PS，或者通过单个图案化工艺同时形成PS和像素限定层的图案。

